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D. Marek
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February 22, 2002

To: Commissioner of Patents and Trademarks
Washington, D.C. 20231

Fr: George O. Saile, Reg. No. 19,572
20 McIntosh Drive
Poughkeepsie, N.Y. 12603

Subject: Serial No. 10/042,073 01/08/02

Dirk Killat

CONVERTER WITH INDUCTOR AND DIGITAL CONTROLLED TIMING

Grp. Art Unit: 2838

INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation In An Application.

The following Patents and/or Publications are submitted to comply with the duty of disclosure under CFR 1.97-1.99 and 37 CFR 1.56. Copies of each document is included herewith.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on February 27, 2002.

Stephen B. Ackerman, Reg.# 37761

Signature/Date Stephen B Ackerman 2/27/02

U.S. Patent 5,757,166 to Sodhi, "Power Factor Correction Controlled Boost Converter with an Improved Zero Current Detection Circuit for Operation Under High Input Voltage Conditions," teaches a power correction factor boost converter. A secondary winding is used for zero current detection of the storage inductor.

U.S. Patent 5,861,734 to Fasullo et al., "Control Architecture for Interleaved Converters," describes a control system for a boost converter using 2 interleaved boost circuits.

U.S. Patent 6,178,104 to Choi, "Power Factor Correction Circuit Using Reverse Sawtooth Waves," describes a power factor correction circuit using reverse saw tooth waves.

U.S. Patent 6,043,633 to Lev et al., "Power Factor Correction Method and Apparatus,", discloses a method and an apparatus for controlling a boost converter that offers power factor correction by compensating for the parasitic capacitance and parasitic oscillations.

Sincerely,

Stephen B Ackerman

Stephen B. Ackerman,
Reg. No. 37761